

Wednesday 28 June

Fast Interlock Module
Session | Location: Lund, ESS site - 1st FI Pacific Water

13:00-13:30 System requirements
Speaker
Rafael Montano

13:30-14:00 Signal conditioning boards
Speaker
Dr Jozsef Molnar

14:00-14:40 Hardware and firmware architecture

15:40

Speaker

Dr Timo Korhonen

14:40-15:10 Interfaces and series units