
BPM Electronics test report

European Spallation Source ESS ERIC

April 4th, 2017

Date	Revision	Description	Authors
04 - April - 2017	0.1	Initial draft.- Preliminary report	Rafael A. Baron,

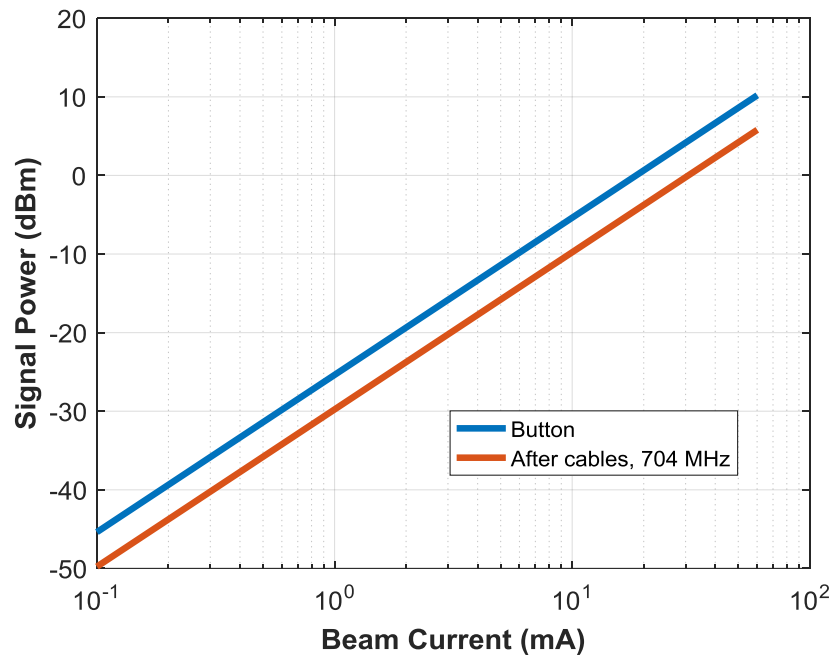


Figure: Typical BPM response for an LWU BPM

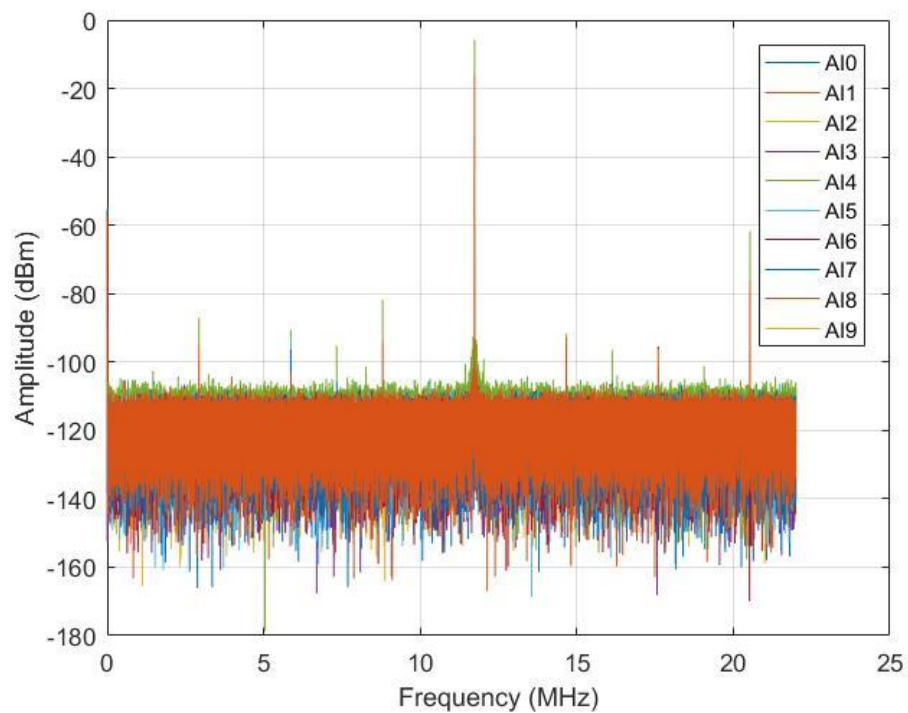


Figure: FFT of the ADC raw data for $IF = 4/15 \cdot 44$ MHz, $RF = 352$ MHz and sampling frequency = 44 MHz.

Figure: BPM electronics position resolution as function of the RF input power, measured performance. The beam current dependence is also shown since it provides the information about the position accuracy error expected from the BPM electronics as function of the beam current. The calibration

factor used for the position calculations is $k_x = k_y = 10 \text{ mm}$. The RMS position resolution is measured for a FPGA DSP with 1 MHz bandwidth.

- The integrated RMS noise provides the useful information about how the noise contribution to the position resolution is distributed over frequency.

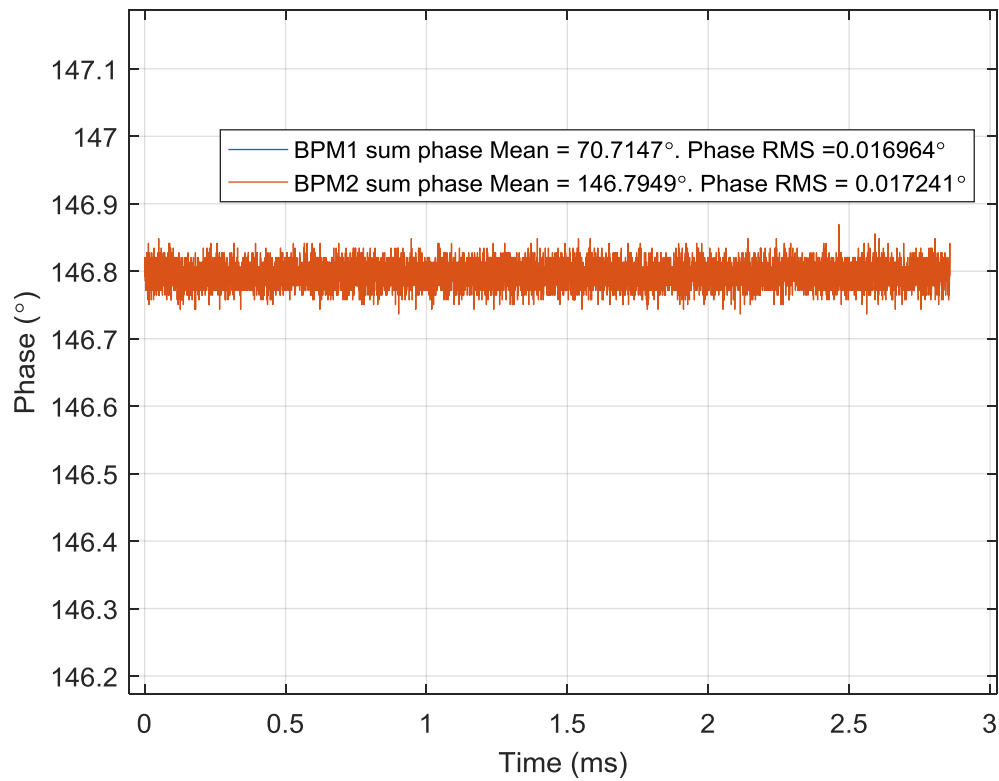


Figure: Phase measurements in time domain for BPM1 and BPM2

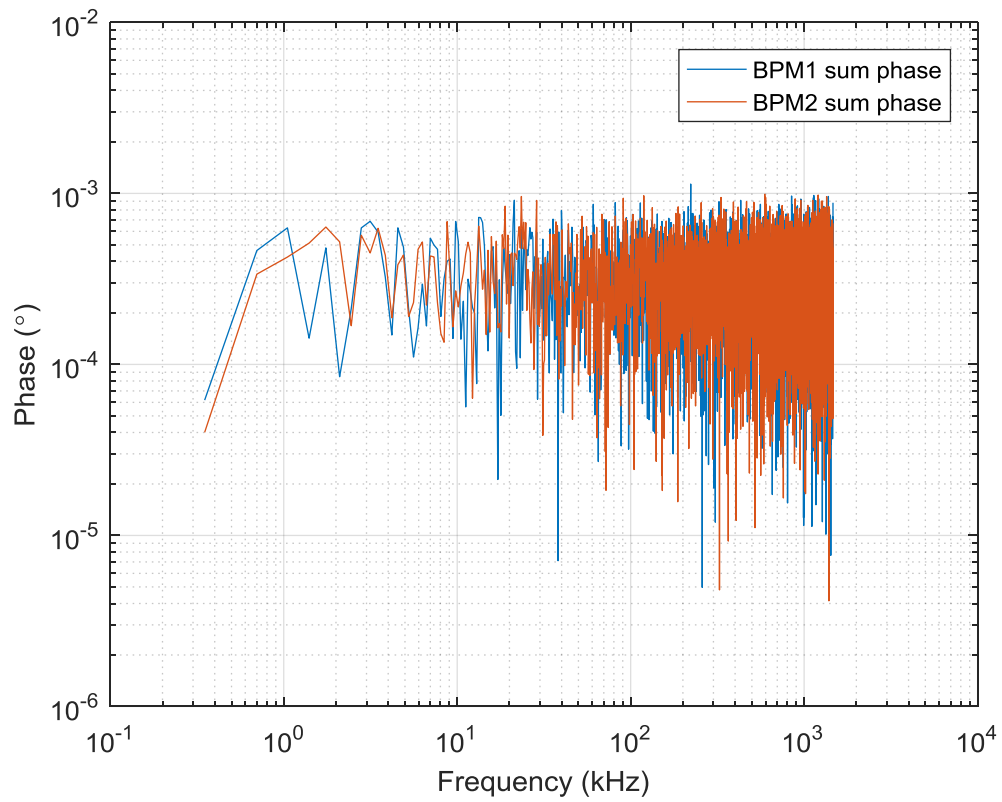


Figure: Phase measurements FFT for BPM1 and BPM2

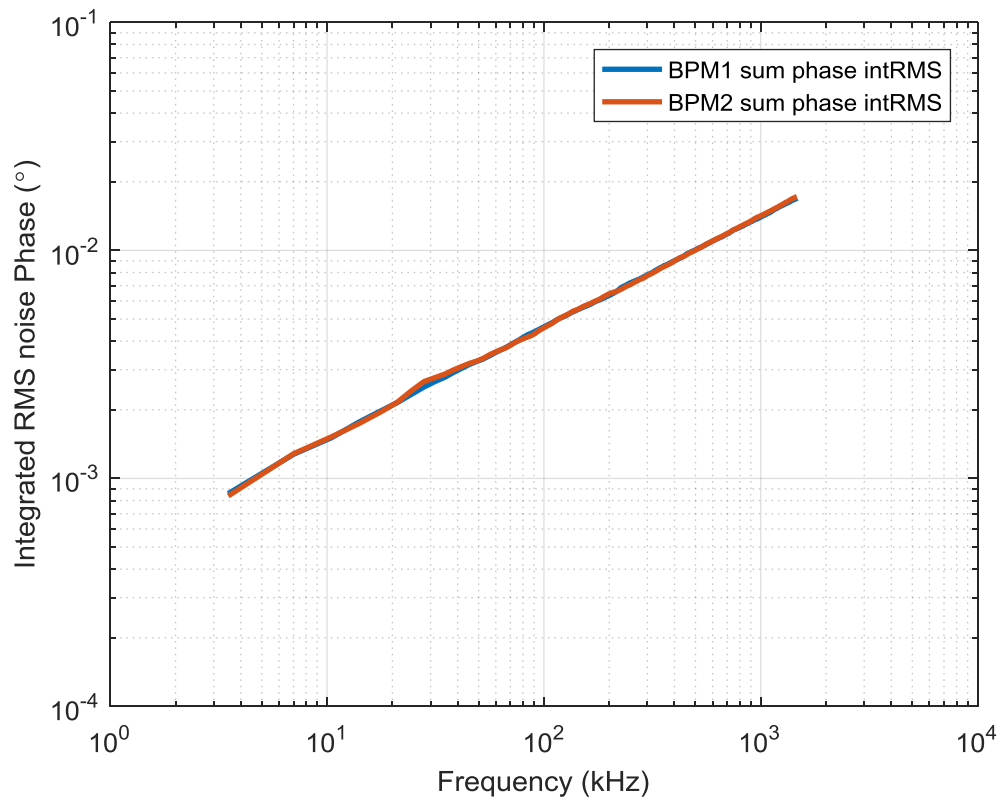


Figure: Phases integrated RMS noise

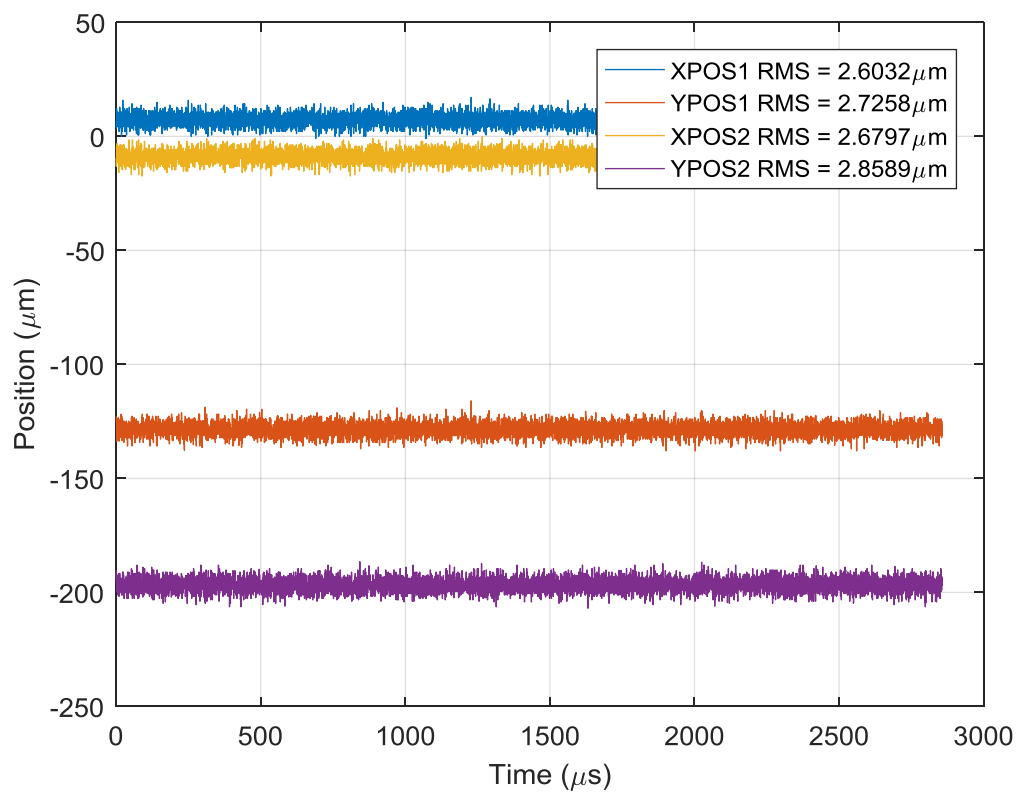


Figure: BPM Positions time domain.

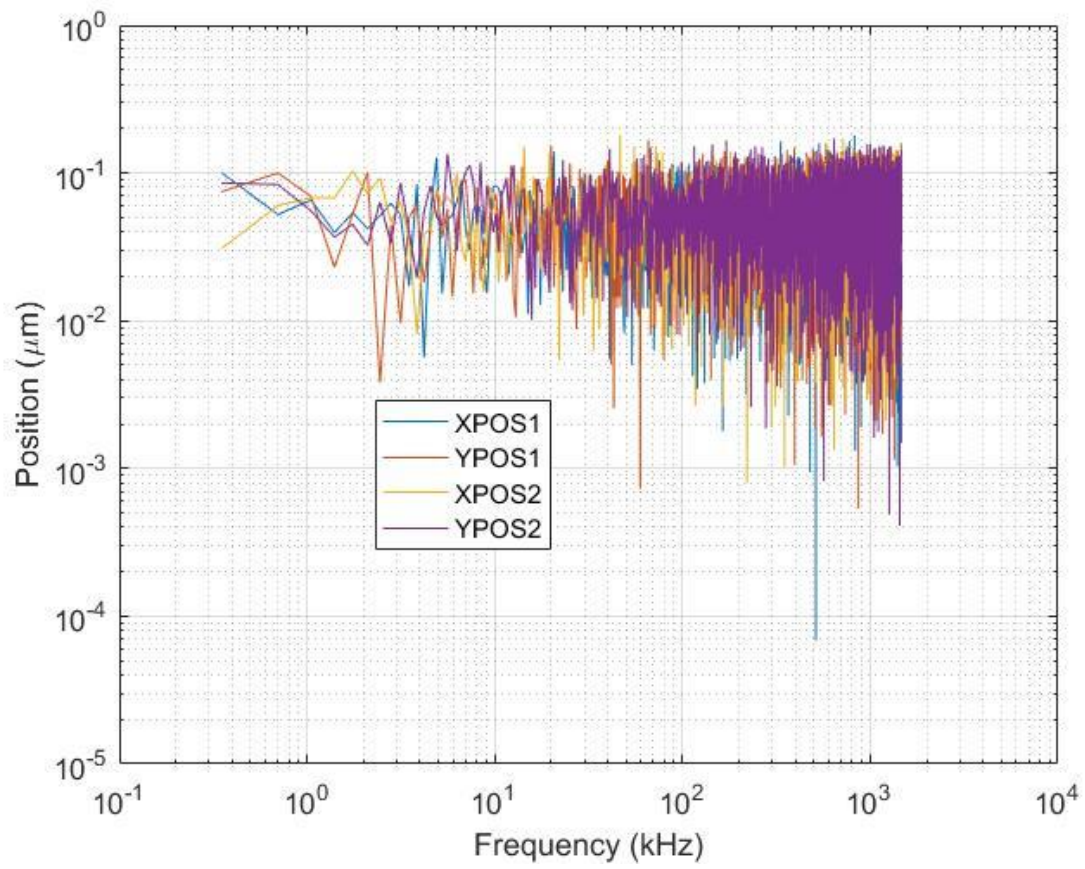


Figure: BPM Positions in frequency domain

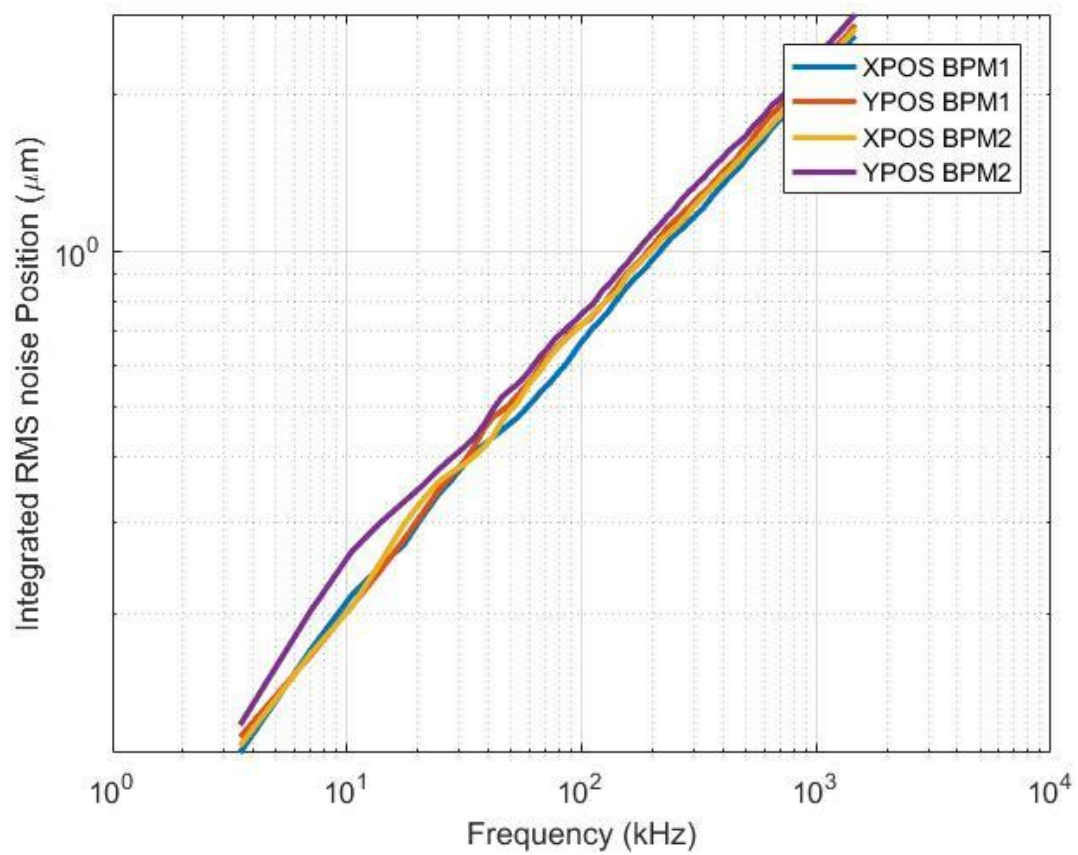


Figure: BPM Positions integrated RMS